

## 1 Pre-Check

This section is designed as a conceptual check for you to determine if you conceptually understand and have any misconceptions about this topic. Please answer true/false to the following questions, and include an explanation:

- 1.1 After calling a function and having that function return, the `t` registers may have been changed during the execution of the function, while `a` registers cannot.
- 1.2 In order to use the saved registers (`s0-s11`) in a function, we must store their values before using them and restore their values before returning.
- 1.3 The stack should only be manipulated at the beginning and end of functions, where the callee saved registers are temporarily saved.

## 2 Calling Convention

Let's review what special meaning we assign to each type of register in RISC-V.

Register	Convention	Saver
<code>x0</code>	Stores <b>zero</b>	N/A
<code>sp</code>	Stores the <b>stack pointer</b>	Callee
<code>ra</code>	Stores the <b>return address</b>	Caller
<code>a0 - a7</code>	Stores <b>arguments</b> and <b>return values</b>	Caller
<code>t0 - t6</code>	Stores <b>temporary</b> values that <i>do not persist</i> after function calls	Caller
<code>s0 - s11</code>	Stores <b>saved</b> values that <i>persist</i> after function calls	Callee

To save and recall values in registers, we use the `sw` and `lw` instructions to save and load words to and from memory, and we typically organize our functions as follows:

```

1 # Prologue
2 addi sp sp -8 # Room for two registers. (Why?)
3 sw s0 0(sp) # Save s0 (or any saved register)
4 sw s1 4(sp) # Save s1 (or any saved register)
5
6 # Code omitted
7
8 # Epilogue
9
10 lw s0 0(sp) #Load s0 (or any saved register)
11 lw s1 4(sp) #Load s1 (or any saved register)
12 addi sp sp 8 #Restore the stack pointer

```

Now, let's see what happens if we ignore calling convention.

2.1 Consider the following blocks of code:

```

1 main:                                1 foo:
2 # Prologue                            2 # Preamble
3 # Saves ra                            3 # Saves s0
4                                        4
5 # Code omitted                        5 # Code omitted
6 addi s0 x0 5                          6 addi s0 x0 4
7 # Breakpoint 1                       7 # Breakpoint 2
8 jal ra foo                            8
9 # Breakpoint 3                       9 # Epilogue
10 mul a0 a0 s0                         10 # Restores s0
11 # Code omitted                      11 jr ra
12
13 # Epilogue
14 # Restores ra
15 j exit

```

- (a) Does `main` always behave as expected, as long as `foo` follows calling convention?
- (b) What does `s0` store at breakpoint 1? Breakpoint 2? Breakpoint 3?
- (c) Now suppose that `foo` didn't have a prologue or epilogue. What would `s0` store at each of the breakpoints? Would this cause errors in our code?

In part (c) above, we saw one way how not following calling convention could make our code misbehave. Other things to watch out for are: assuming that `a` or `t` registers will be the same after calling a function, and forgetting to save `ra` before calling a function.

Function `myfunc` takes in two arguments: `a0`, `a1`. The return value is stored in `a0`. In `myfunc`, `generate_random` is called. It takes in 0 arguments and stores its return value in `a0`.

```

1 myfunc:
2     # Prologue (omitted)
3
4     addi t0 x0 1
5     slli t1 t0 2
6     add t1 a0 t1
7     add s0 a1 x0
8
9     jal generate_random
10
11    add t1 t1 a0
12    add a0 t1 s0
13
14    # Epilogue (omitted)
15    ret

```

- 2.2 Which registers, if any, need to be saved on the stack in the prologue?
- 2.3 Which registers do we need to save on the stack before calling `generate_random`?
- 2.4 Which registers need to be recovered in the epilogue before returning?

### 3 Recursive Calling Convention

Write a function `sum_square` in RISC-V that, when given an integer `n`, returns the summation below. If `n` is not positive, then the function returns 0.

$$n^2 + (n - 1)^2 + (n - 2)^2 + \dots + 1^2$$

To implement this, we will use a tail-recursive algorithm that uses the `a1` register to help with recursion. More specifically, you will be writing the following function:

sum_squares_recursive: Return the value $m + n^2 + (n - 1)^2 + (n - 2)^2 + \dots + 1^2$		
<b>Arguments</b>	<code>a0</code>	A 32-bit number. $n$ . You may assume $n \leq 10000$ .
	<code>a1</code>	A 32-bit number. $m$ .
<b>Return value</b>	<code>a0</code>	$m + n^2 + (n - 1)^2 + (n - 2)^2 + \dots + 1^2$ . If $n \leq 0$ , return $m$ .

When the above function is called with `a1` set to 0, we will get the behavior that we expect. For this problem, you are given a RISC-V function called `square` that takes in a single integer and returns its square.

square: Square a number		
<b>Arguments</b>	<code>a0</code>	$n$ .
<b>Return value</b>	<code>a0</code>	$n^2$

3.1 Since this is a recursive function, let's start with the base case of our recursion.

```
sum_squares:
    ----- zero_case

    # To be implemented in the next question.

zero_case:
    -----
    jr ra
```

3.2 Next, implement the recursive logic. *Hint: If you let  $m' = m + n^2$ , then*

$$m + n^2 + (n - 1)^2 + \dots + 1^2 = m' + (n - 1)^2 + \dots + 1^2$$

```
sum_squares:
    # Handle zero case (previous question)
    mv t0 a0
    jal ra -----

    add a1 a0 a1
    addi a0 t0 -1

    jal ra -----
    jr ra
zero_case:
    # Handle zero case (previous question)
    jr ra
```

3.3 Now, think about calling convention from the caller perspective. After the call to `square`, what is in `a0`? `a1`? Which one of the registers will cause a calling convention violation?

3.4 What about the recursive call? What will be in `a0` after the call to `sum_squares`? `a1`?

3.5 Now, go back and fix the calling convention issues you identified. Note that not all blank lines may be used. There may also be another caller saved register that you need to save as well!

```
sum_squares:
    # Handle zero case (previous question)
    mv t0 a0
    -----
    -----
    -----
    -----
    # (previous question)
    jal ra -----
    -----
    -----
    -----

    add a1 a0 a1
    addi a0 t0 -1

    -----
    -----
    -----
    # (previous question)
    jal ra -----
    -----
    -----

zero_case:
    # Handle zero case (previous question)
    jr ra
```

- 3.6 Now, from a callee perspective, do we have to save any registers in the prologue and epilogue? If yes, what registers do we have to save, and where do we place the prologue and epilogue? If no, briefly explain why.