CS61C Fall 2025

RISC-V Calling Convention

Discussion 5

1 RISC-V Calling Convention

1.1 Consider the following blocks of code:

```
main:
                                                    foo:
  # Prologue
                                                      # Prologue
  # Saves ra
                                                      # Saves s0
  # Code omitted
                                                      # Code Omitted
  addi s0 x0 5
                                                      addi s0 x0 4
  # Breakpoint 1
                                                      # Breakpoint 2
  jal ra foo
  # Breakpoint 3
                                                      # Epilogue
  mul a0 a0 s0
                                                      # Restores s0
  # Code omitted
                                                      jr ra
  # Epilogue
  # Restores ra
  j exit
```

a) Does main always behave as expected, as long as foo follows calling convention?

Yes, since **foo** saves the saved registers and **main** saves the return address.

b) What does **s0** store at breakpoint 1? Breakpoint 2? Breakpoint 3?

```
Breakpoint 1: 5, Breakpoint 2: 4, Breakpoint 3: 5
```

c) Now suppose that **foo** didn't have a prologue or epilogue. What would **s0** store at each of the breakpoints? Would this cause errors in our code?

Breakpoint 1: 5, Breakpoint 2: 4, Breakpoint 3: 4. This would cause errors because we rely on **s0** having the value of 5 in our calculations in **main**.

In part (c) above, we see one way how not following calling convention could make our code misbehave. Other things to watch out for are: assuming that a or t registers will be the same after calling a function, and forgetting to save ra before calling a function.

Function myfunc takes in two arguments: a0, a1. The return value is stored in a0. In myfunc, generate_random is called. It takes in 0 arguments and stores its return value in a0.

```
myfunc:
# Prologue (omitted)
addi t0 x0 1
slli t1 t0 2
add t1 a0 t1
add s0 a1 x0

jal generate_random
add t1 t1 a0
add a0 t1 s0

# Epilogue (omitted)
ret
```

a) Which registers, if any, need to be saved on the stack in the prologue?

```
Answer: s0, ra
```

We must save all s-registers we modify. In addition, if a function contains a function call, register ra will be overwritten when the function is called (i.e. jal ra label). ra must be saved before a function call. It is conventional to store ra in the prologue (rather than just before calling a function) if the function contains a function call. myfunc contains the function call generate_random.

b) Which registers, if any, need to be saved on the stack before calling generate_random?

Answer: t1

Under calling conventions, all the t-registers and a-registers may be changed by generate_random, so we must store all of these which we need to know the value of after the call. A total of 2 t-registers are used before calling generate_random, t0 and t1, but only t1's value is referenced again after the function call.

c) Which registers, if any, restored from the stack in the epilogue before returning?

```
Answer: s0, ra
```

This mirrors what we did in the prologue.

2 Recursive Calling Convention

Write a function **sum_squares** in RISC-V that, when given an integer **n** and a constant **m**, returns the summation below. If **n** is not positive, then the function returns m.

$$m+n^2+(n-1)^2+(n-2)^2+\ldots+1^2$$

To implement this, we will use a tail-recursive algorithm that uses the **a1** register to help with recursion.

$\verb"sum_squares_recursive":$ Return the value $m+n^2+(n-1)^2++1^2$				
Arguments	a0	A 32-bit number n . You may assume $n \leq 10000$.		
	a1	A 32-bit number m .		
Return value	value a0 $m + n^2 + (n-1)^2 + (n-2)^2 + + 1^2$. If $n \le 0$, return m			

For this problem, you are given a RISC-V function called **square** that takes in a single integer and returns its square.

square: Squares a number				
Arguments	a0	n		
Return value	a0	n^2		

2.1 Since this a recursive function, let's implement the base case of our recursion:

```
sum_squares:
bge x0 a0 zero_case

# To be implemented in the next question
zero_case:
mv a0 a1
jr ra
```

2.2 Next, implement the recursive logic. *Hint:* if you let $m' = m + n^2$, then

$$m + n^2 + (n - 1)^2 + \dots + 1^2 = m' + (n - 1)^2 + \dots + 1^2$$

```
sum_squares:
# Handle zero case (previous question)
bge x0 a0 zero_case

mv t0 a0
jal ra square

add a1 a0 a1
addi a0 t0 -1

jal ra sum_squares
jr ra

zero_case:
# Handle zero case (previous question)
jr ra
```

4 RISC-V Calling Convention

2.3 Now, think about calling convention from the caller perspective. After the call to **square**, what is in **a0** and **a1**? Which one of the registers will cause a calling convention violation?

a0 will contain n^2 , and a1 will contain garbage data, causing a calling convention violation. The register t0 will also hold garbage, which would also cause a calling convention violation.

2.4 What about the recursive call? What will be in a0 and a1 after the call to sum_squares?

a0 will contain $m+n^2+\ldots+1^2$, and a1 will contain garbage data. However, since a0 now contains the expected return value, we no longer care about the value in a1, and can directly return. It is the job of whichever function called **sum_squares** to deal with saving caller-saved registers if they are needed in the future.

2.5 Now, go back and fix the calling convention issues you identified. Note that not all blank lines may be used. There may also be another caller saved register that you need to save as well!

```
sum_squares:
# Handle zero case (previous question)
mv t0 a0
# Save caller saved registers on the stack
addi sp sp -12
sw a1 0(sp)
sw t0 4(sp)
sw ra 8(sp)
jal ra square
# Restore register and stack
lw a1 0(sp)
lw t0 4(sp)
lw ra 8(sp)
addi sp sp 12
add a1 a0 a1
addi a0 t0 -1
# Save caller saved registers on the stack
# Note that we don't need to save a1 and t0
# because we do not need their values
# after the function call.
addi sp sp -4
sw ra O(sp)
jal ra sum_squares
# Restore caller saved registers on the stack
lw 0(sp)
addi sp sp 4
jr ra
zero_case:
# Handle zero case (previous question)
jr ra
```

Now, from a callee perspective, do we have to save any registers in the prologue and epilogue? If yes, what registers do we have to save, and where do we place the prologue and epilogue? If no, briefly explain why.

No, we do not have to take callee saved registers into account because we do not use any callee saved registers. However, since we call two functions, it is possible to save ra in the prologue and restore it in an epilogue immediately before the jr ra before the zero_case label.